



Atty. Docket No. YOR9-2000-0054
(590.0007)

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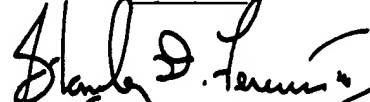
THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Bergamaschi et al.
Serial No. : 09/542,024 Examiner : Unassigned
Filed : April 3, 2000 Art Unit : Unassigned
For : METHODS AND ARRANGEMENTS FOR
AUTOMATICALLY INTERCONNECTING CORES
IN SYSTEMS-ON-CHIP

July 23, 2001

REQUEST FOR CORRECTED FILING RECEIPT

I hereby certify that this correspondence and any documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, addressed to the Assistant Commissioner for Patents, Washington, DC 20231 on July 23, 2001.


Stanley D. Ference III
Reg. No. 33,879

July 23, 2001
Date of Signature


Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Attached is a copy a Corrected Official Filing Receipt dated September 12, 2000, received from the Patent and Trademark Office in the above identified application. Applicants did not request the September 12, 2000, Corrected Filing Receipt, and this Corrected Filing Receipt introduces additional inventors and changes the title of the application. The correct title is used in this document.

The information to be changed is shown in red on the attached copy of the September 12, 2000, Corrected Filing Receipt. Also attached is a copy of a Corrected Filing Receipt dated August 24, 2000, on which the correct data appears. Please update your records accordingly and issue a corrected filing receipt.

Respectfully submitted,



Stanley D. Ference III
Registration No. 33,879

FERENCE & ASSOCIATES
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Attorneys for Applicant

Enclosures



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/542,024	04/03/2000	2782	820	YOR-2000-0054	4	18	3

Ference & Associates
129 Oakhurst Road
Pittsburgh, PA 15215

CORRECTED FILING RECEIPT



OC00000005391677

Date Mailed: 09/12/2000

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Applicant(s)

Reinaldo A. Bergamaschi, Tarrytown, NY ;
Subhrajit Bhattacharya, White Plains, NY ;
~~Jean Marc R. Davcau, Grenoble, FRANCE ;~~
~~William R. Lee, Apex, NC ;~~

Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 06/06/2000

Title

Methods and arrangements for ~~automatic synthesis of systems on chip~~

*automatically interconnecting cores in
systems-on-chip*

Preliminary Class

712

Data entry by : JOHNSON, TRACEY

Team : OIPE

Date: 09/12/2000





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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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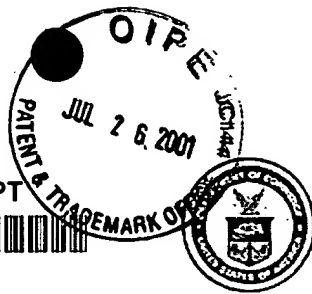
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CORRECTED FILING RECEIPT



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Patent and Trademark OfficeAddress: ASSISTANT SECRETARY AND
COMMISSIONER OF PATENT AND TRADEMARKS
Washington, D.C. 20231

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/542,024	04/03/2000	2782	820	YOR-2000-0054	4	18	3

Ference & Associates
129 Oakhurst Road
Pittsburgh, PA 15215

Date Mailed: 08/24/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

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Subhrajit Bhattacharya, White Plains, NY ;

Continuing Data as Claimed by Applicant

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Title

Methods and arrangements for automatically interconnecting cores in systems-on-chip

Preliminary Class

712

Data entry by : SMALLWOOD, EAON

Team : OIPE

Date: 08/24/2000



Correct Data Appears



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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Bib Data Sheet

SERIAL NUMBER 09/542,024	FILING DATE 04/03/2000 RULE -	CLASS 712	GROUP ART UNIT 2782	ATTORNEY DOCKET NO. YOR-2000-0054
APPLICANTS Reinaldo A. Bergamaschi, Tarrytown, NY ; Subhrajit Bhattacharya, White Plains, NY ; Jean-Marc R. Daveau, Grenoble, FRANCE; William R. Lee, Apex, NC ;				
** CONTINUING DATA ***** ** FOREIGN APPLICATIONS *****				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/06/2000				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u> </u> Examiner's Signature <u> </u> Initials		STATE OR COUNTRY NY	SHEETS DRAWING 4	TOTAL CLAIMS 18
			INDEPENDENT CLAIMS 3	
ADDRESS Ference & Associates 129 Oakhurst Road Pittsburgh ,PA 15215				
TITLE Methods and arrangements for automatic synthesis of systems-on-chip				
FILING FEE RECEIVED 820	FEES: Authority has been given in Paper No. <u> </u> to charge/credit DEPOSIT ACCOUNT No. <u> </u> for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <u> </u> <input type="checkbox"/> Credit	